

# Alpha Networks Inc.

STX-60x0-486F

48-port SFP+ with 6-port 100G QSFP28 (TOR/Aggregation Switch)

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## **Revision History**

Version	Revised Date	Author	Content Revised
0.1	01/19/2017	Natalie Chang	Initial Version



## Scope

This documents defines the technical specification for STX-60x0-486F used in the Open Compute Project as 10G Top of the Rack (ToR) or as an aggregation switch

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#### Overview

The STX-60x0-486F Series are leaf switch or aggregation switch for Data Center, Telco and Enterprise. It provides a total combined bandwidth of 2,160 Gbps, feature 48 ports of 10 Gbps with 6 ports of 100 Gbps at wire-speeds Ethernet. The system also provides a Mini-USB (Type B) console port and an out-of-band (OOB) management port.

The STX-60x0-486F switch supports 1+1 redundant power supplies and 5+1 redundant fan modules to provide the system reliability.

The CPU was designed as modules, and the following CPU modules are supported.

- Intel Rangeley CPU based (C2558, 4 cores/2.4GHz), model number: STX-60A0-486F
- Intel Broadwell DE CPU based (D1518, 4 cores/2.3GHz), model number: STX-60B0-486F

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Items		Detailed Description		
MAC Controller		BCM88375*1		
Packet DRAM		8GB		
TCAM		NL88659 * 2		
Octal Re-Timer		BCM82328*1		
вмс		ASPEED AST1250*1		
PHY for 100G		N/A, PHY-less design		
MAC for Combo Ma	nagomont	Intel I210*1		
IVIAC IOI COMBO IVIA	nagement	BCM53115M*1		
CPU I		Module for STX-60A0-486F		
Modular CPU	CPU	Intel Rangeley C2558 Quad Cores/2.4GHz		
board (option2) RAM		DDR3 4GB for Intel Rangeley CPU(reserved to 32GB)		



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	Flash	M.2 SATA SSD 32GB for Intel Rangeley CPU(reserved to 64GB)
	Boot Flash	8MB for Intel Rangeley CPU(reserved to 16MB)
	CPU I	Module for STX-60B0-486F
	CPU Intel Broadwell DE D-1518 Quad Cores/2.3GI (Designed reserved up to 8 cores)	
Modular CPU	RAM	DDR4 8GB (reserved up to TBD GB)
board (option 1)	Flash	M.2 SATA SSD 32GB for Intel Broadwell CPU (reserved up to TBD GB)
	Boot Flash	16MB for Intel Broadwell CPU(reserved to 32MB)

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## 1 Feature Highlights

The STX-60x0-486F Series are leaf switch or aggregation switch for Data Center, Telco and Enterprise. It provides a total combined bandwidth of 2,160 Gbps, feature 48 ports of 10 Gbps with 6 ports of 100 Gbps at wire-speeds Ethernet. The system also provides a Mini-USB (Type B) console port and an out-of-band (OOB) management port.

- OCP micro server modular CPU board with large flash and memory
- Temperature warning via software-readable thermal monitor
- Real time clock (RTC) support
- Two Hot-swappable redundant power supply
- Six fan modules, (5+1) redundancy
- All I/O and management ports located on the front panels
- One R/J-45 GE out of band management port
- One Mini-USB (Type B) console port
- One USB (Type A) for storage device
- One reset button
- 48 port of SFP+ for 10Gbps Ethernet
- 6 port of QSFP28 for 100Gbps Ethernet



## 2 Physical Overview

### 2.1 Mechanical Dimension

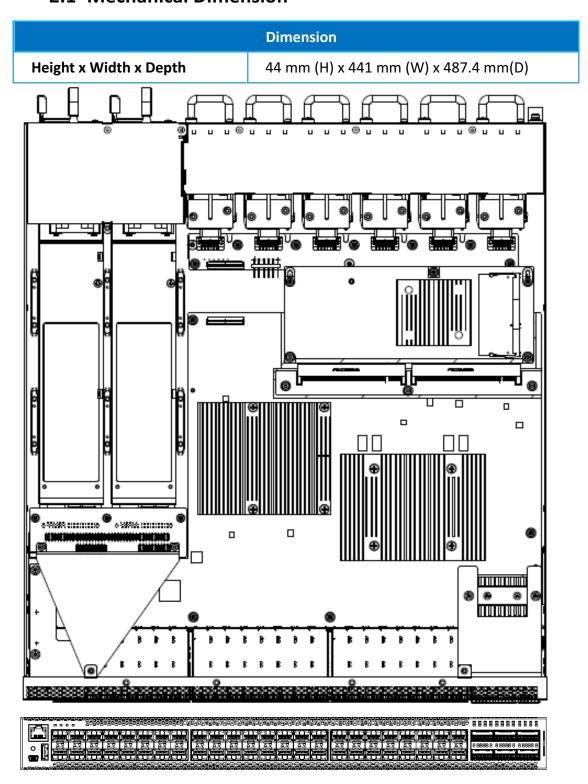
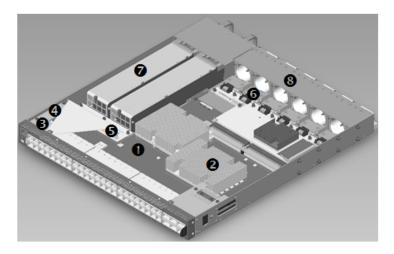


Figure 1: STX-60x0-486F chassis dimension



### 2.2 Top View



Number	Items
1	Main Board
2	CPU Board
3	MGMT Board
4	LED Board
5	PSU Extender Board
6	Fan Extender Board
7	PSU x 2
8	FAN x 6

Figure 2: STX-60x0-486F top view

#### 2.3 Front View

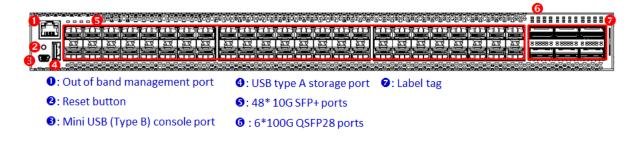
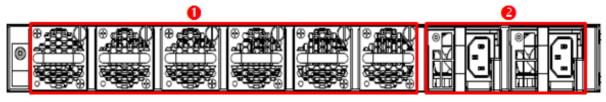


Figure 3: STX-60x0-486F front view

### 2.4 Rear View



- Hot swappable FAN modules
- 2 Hot swappable power supply

Figure 4: STX-60x0-486F rear view

### 3 LED Definition

The following table defines the per device LEDs' behaviors:

Items	LED Indication	Color	Behavior	Description
1001110			201141101	2000.000



1		Blue	Blinking	Locator function is enable
1	1 Locator		Off	Locator function is disable
			Solid Light	POST Passed, normal operation
		Green	Blinking	POST in progress
			Light off	System No power
2	STAT	Amber	Blinking	POST failed or overheat or power supply failed or Fan module fail or over temperature
			Solid Light	The Reset button is recessed
	PWR	Green	Solid Light	Power On
3			Off	Power Off and no power attached
3		Amber	Blinking	Power supply failures, over voltage, over current, over temperature
	FAN	Green	Solid Light	All diagnostics pass. The module is operational.
4			Off	The module is not receiving power
		Amber	Blinking	Failure
			Solid Light	Link up
5	MGMT Link	Green(R)	Blinking	Packet transmitting or receiving
			Light off	No link up or port disable
6	MGMT Speed	Green(R)	Off	10M speed
	Wiowii Speed	Green(IX)	On	100M/1000G speed

Table 1: LED behavior for system

## The following defines the 10G SFP+ and 100G QSFP28 Ethernet port LEDs' behaviors:

Location	LED Indication	Color	Behavior	Description
	Link/Act/Speed	Green	Solid Light	A transceiver module or cable has been correctly installed. The port has a link and is operating at 10Gbps
IED			Blinking	The port is sending or receiving data at 10Gbps
LED Port 1~48 (10G bps)		Amber	Solid light	A transceiver module or cable has been correctly installed. The port has a link and is operating at 1Gbps
			Blinking	The port is sending or receiving data at 1Gbps
		Off	Light off	Link down or no link
LED number	Link/Act/Speed	White	Solid Light	When there is a secure 100G connection (or link)

49~54, group of 1 (100Gbps)			Blinking	Packet transmitting or receiving
LED number	Link/Act/Spood	Blue	Solid Light	When there is a secure 40G connection (or link)
49~54, group of 1 (40Gbps)	Link/Act/Speed	blue	Blinking	Packet transmitting or receiving
LED number	Link/Act/Speed	Amber	Solid Light	When there is a secure 50G connection (or link)
49~54, group of 1 (50Gbps)	Link/Act/Speed	Amber	Blinking	Packet transmitting or receiving
LED number 49~54, group of 1	Link/Act/Speed	Green	Solid Light	When there is a secure 10G (40G Mode)/25G (100G Mode) connection (or link)
(10G/25Gbps)			Blinking	Packet transmitting or receiving
49~54, group of 1			Light off	No link up or port disable

Table 2: LED behavior for 10G & 100G Ethernet port

Each power supply module has a bi-color LED, which behavior is descript in the following:

LED Color	Behavior	Description		
	Solid Light	Output ON and OK		
Green	Blinking	AC present / AC Line 12VSB Holdup		
	Light off	No AC power to all power supplies		
	Solid Light	Power supply critical event causing a shutdown; failure, Fan Fail		
Amber	Blinking	Power supply warning events where the power supply continues to operate; high temp, high power, high current, slow fan.		

Table 3: Power supply LED definition

## **4 Field Replaceable Components**

## **4.1 Power Supply Modules**

The switch is powered through one or two internal power supply modules.

Supported power supply modules:

- AC-770-12-FB
- AC-770-12-BF



- DC-1100-12-FB
- DC-1100-12-BF

The switch requires only one power supply for its operations, but you can include a second one for redundancy. By default the switch is installed one power supply in the second power supply slot, and filled the first slot with a filler panel. You can order extra power supplies with forward airflow or reverse airflow. Be sure to order the same direction of airflow as is used with the switch. Never leave a power supply slot empty. Please fill the slot with a filler panel.

Please refer to Table 4 for the recommended power supply usage for each switch model.

Switch Model Number	Equipped CPU Model	Power Supply Vendor	Power Supply Model Number	Minimum Number of Power Supply Need
STX-60A0-486F-AF-X	Intel Rangely C2558	Delta Electronics	AC-770-12-FB	1
STX-60A0-486F-AB-X	Intel Rangely C2558	Delta Electronics	AC-770-12-BF	1
STX-60A0-486F-DF-X	Intel Rangely C2558	Delta Electronics	DC-1100-12-FB	1
STX-60A0-486F-DB-X	Intel Rangely C2558	Delta Electronics	DC-1100-12-BF	1
STX-60B0-486F-AF-X	Intel Broadwell DE D-1518	Delta Electronics	AC-770-12-FB	1
STX-60B0-486F-AB-X	Intel Broadwell DE D-1518	Delta Electronics	AC-770-12-BF	1
STX-60B0-486F-DF-X	Intel Broadwell DE D-1518	Delta Electronics	DC-1100-12-FB	1
STX-60B0-486F-DB-X	Intel Broadwell DE D-1518	Delta Electronics	DC-1100-12-BF	1

Table 4: Power supplies usage

Power Supply connector: Molex 45984-4343

Pin#	Description	Pin #	Descriptin3
<b>S1</b>	+12VRS+	S13	N/A
<b>S2</b>	+12VRS-	<b>S14</b>	SGND
<b>S3</b>	12LS	S15	A0
<b>S4</b>	SMB ALERT	S16	N/A
<b>S5</b>	SDA	S17	Vs
S6	SCL	S18	N/A
<b>S7</b>	PSKILL	S19	N/A
<b>S8</b>	PSON	S20	N/A
<b>S9</b>	PWOK	S21	N/A



S10	A1	S22	N/A
S11	5VSB	S23	+5VSB
S12	5VSB	S24	+5BSB
P1	+12_VOUT	Р3	GND
P2	+12_VOUT	P4	GND

Table 5: Power supply connector pin out

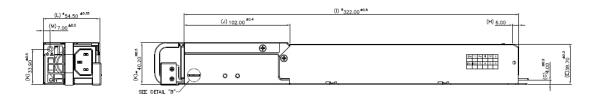


Figure 5: Power supply mechanical specification

#### LED definition on power supply

Power Supply Condition	LED Status
PSU is switched on and is running (Output ON and OK)	Green
No AC power to all power supplies or PSU is ON but with warning events	Off
PSU is OFF but 5VSB is on	1Hz on Blink Green
PSU critical event causing a shutdown: failure, OCP, OTP, OVP, UVP, Fan Fail	Amber
PSU warning events: high temp, high power, high current, slow fan, under input voltage	1Hz on Blink Amber

Table 6: LED definition on power supply

#### FRU

FRU data format compliant with IPMI version 1.0 (per rev 1.1 from Sept. 25, 1999) specification. The FRU device will implement the same protocols as the commonly used AT24C02 device, including the Byte Read, Sequential Read, Byte Write and Page Read protocols.

#### The EEPROM content is as following

Area Type	Description	
Common Header	Format Version Number	
Internal Use Area	Not required, do not reserve	



Chassis Info Area	Not applicable, do not reserve		
Board Info Area	Not applicable, do not reserve		
D 1 11 C A	As defined by the IPMI FRU document.		
Product Info Area	Product information is defined as following		
Field name	Field Description		
Manufacturer Name	{Formal name of manufacturer}		
Product Name	{Manufacturer's model number}		
Product part/model number	Customer part number		
Product Version	Customer current revision		
Product Serial Number	{Defined at time of manufacture}		
Asset Tag	{Not used, code is zero length byte}		
FRU File ID	{Not required}		
DAD D. L.	{Added as necessary to allow for 8-bype offset to next		
PAD Bytes	area}		
	As defined by the IPMI FRU documentation. The		
	following information shall be used by this power		
	supply:		
Mulit-Record Area	Power Supply Information (Record type 0x00)		
	DC Output (Record Type 0x01)		
	No other record types are required for power supply		
	Multi-Record information shall be defined as following		
Field Name (PS Info)	Field Information Definition		
Overall Capacity (watts)	770		
Peak VA	770		
Inrush current (A)	40		
Inrush interval (msec)	5		
Low end input voltage range 1	100		
High end input voltage range 1	127		
Low end input voltage range 2	200		
High end input voltage range 2	240		
Low End Input Frequency	47		
Range	47		
High End Input Frequency	62		
Range	63		
Holdup Time (msec)	12		



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Binary flags	Set for: Hot Swap support, Auto switch and PFC		
Peak Wattage	839 Watts		
Peak Wattage Time in seconds	12		
Combined wattage	770		
Predictive fail tech support	Supported		
Field Name (Output)	Field Description : Two output are to be defined from		
Field Name (Output)	#1 to #2, as follows: +12V and +5VSB		
Output Information	Set for: Standby on +5VSB, no 5VSB on all others		
All other output fields	Format per IPMI specification , using parameters in		
All other output fields	this specification		

Table 7: Power Supply EEPROM FRU data format

#### 4.2 Fan Modules

The STX-60x0-486F supports up to 5+1 fan modules. For front to rear and rear to front air flow, different types of fan modules are required.

Air Flow Direction	Part Number
Front to Rear	AVC DFTA0456B2UP152
Rear to Front	AVC DFTA0456B2UP145

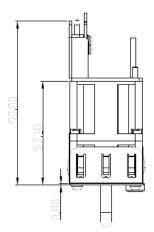
Table 8: Fan Modules part number

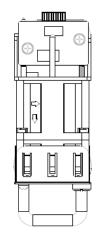
Fan module connector: LCU SM401V-20P

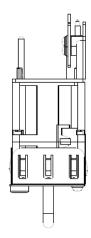
#	Name	Description	#	Name	Description
1	FAN_CON_TACH_0	FAN tachometer 0	11	FAN_DIR	FAN Direction
2	GND	GND	12	GND	GND
3	FAN_12VIN	12V	13	FAN_12VIN	12V
4	FAN_CON_PWM_0	PWM control for FAN0	14	EE_GND	EEPROM GND
5			15	EE_SDA	EEPROM SDA
6	EE_SCL	EEPROM SCL	16	EE_VDD	EEPROM VDD
7	EE_A0	EEPROM ADDR_0	17	FAN_CON_PWM_1	PWM control for FAN1
8	FAN_12VIN	12V	18	FAN_12VIN	12V
9	GND	GND	19	GND	GND
10	FAN_PRESENT#	Exist FAN module	20	FAN_CON_TACH_1	FAN tachometer 0

Table 9: Fan Modules connector pin out









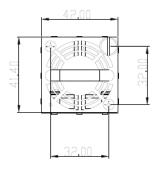


Figure 6: Fan module mechanical specification

## **5 System Overview**

The STX-60x0-486F comprised of the following PCB

PCB Function	PCB Layer	Dimension (L*W)mm
Main board	22	328.8mm*431.5mm
FAN module	2	38.5*29
FAN extend board	4	277*30
LED board	2	110.5*70
PSU board	4	41.3*132.4
CPU transfer board	4	223*16
CPU board (Rangeley)	12	210*90
CPU board (Broadwell DE)	12	210*100
Management board	2	112.1*57

Table 10: PCBs for STX-60x0-486F



### 5.1 Main PCB

The main PCB is a 22 layer PCB where the switch MAC resides. It also supports the following functions:

- Networking I/O ports
- Connectivity to Management ports
- LED
- Connectivity to power supply and fan
- Power conversion circuit
- Etcs

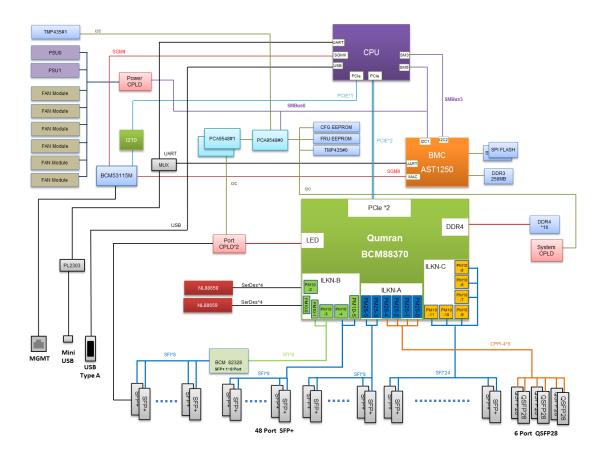


Figure 7: Main board block diagram



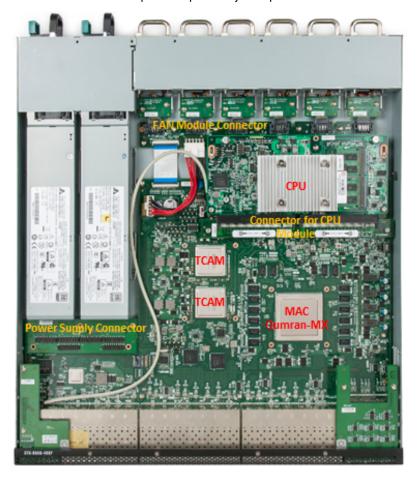


Figure 8: Main PCB top view

## 5.2 CPU Subsystem

The following Table is the list of key components used in STX-60x0-486F

CPU Module for STX-60A0-486F					
	CPU	Intel Rangeley C2558 Quad Cores/2.4GHz			
Modular CPU	RAM	DDR3 4GB for Intel Rangeley CPU(reserved to 32GB)			
board (option 2)	Flash	M.2 SATA SSD 32GB for Intel Rangeley CPU(reserved to 64GB)			
	Boot Flash	8MB for Intel Rangeley CPU(reserved to 16MB)			
	CPU Module for STX-60B0-486F				
	CPU	Intel Broadwell DE D-1518 Quad Cores/2.3GHz (Designed reserved up to 8 cores)			
Modular CPU	RAM	DDR4 8GB (reserved up to TBD GB)			
board (option1)	Flash	M.2 SATA SSD 32GB for Intel Broadwell CPU			
	ridəli	(reserved up to TBD GB)			
Boot Flash		16MB for Intel Broadwell CPU(reserved to 32MB)			

Table 11: CPU subsystem key Components



## 5.2.1 Management CPU Board

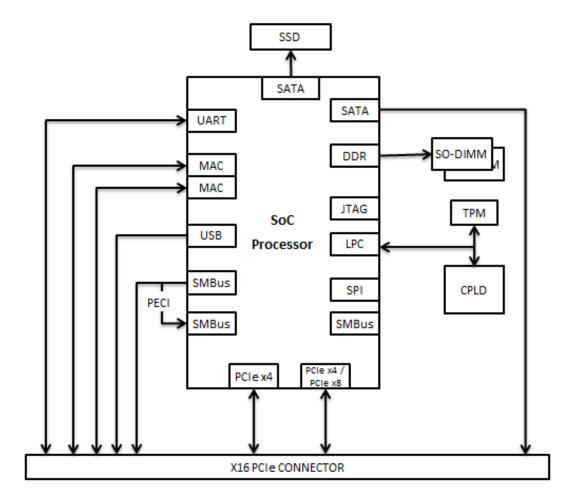


Figure 9: CPU Module Block Diagram

Intel CPU module connector: x16 PCIe Edge Connector

For 80 PIN connector			
#	Pin Name	#	Pin Name
B1	P12V	A1	PRSNT#
B2	P12V	A2	P12V
В3	P12V	А3	P12V
B4	GND	A4	GND
B5	I2C_SCL	<b>A5</b>	RFU
В6	I2C_DATA	A6	RFU
В7	GND	A7	COM_TX
B8	PWR_BTN#	A8	COM_RX
В9	USB_P	A9	GE0_MDC
B10	USB_N	A10	GE0_MDIO



B11	SYS_RESET#	A11	PCIEO RESET#	
DII	JIJ_NEJET#	KEY	I GILO_INESE I#	
B12	<b>B12</b> I2C ALERT# <b>A12</b> GND			
B13	GND	A13	PCIEO REFCLK P	
B14	GND	A14	PCIEO_REFCLK_N	
B15	PCIEO_TXO_P	A15	GND	
B16	PCIEO_TXO_N	A16	GND	
B17	GND	A17	PCIEO RXO P	
B18	GND	A18	PCIEO_RXO_N	
B19	PCIEO_TX1_P	A19	GND	
B20	PCIEO_TX1_N	A20	GND	
B21	GND	A21	PCIEO_RX1_P	
B22	GND	A22	PCIEO_RX1_N	
B23	PCIEO_TX2_P	A23	GND	
B24	PCIEO_TX2_N	A24	GND	
B25	GND	A25	PCIEO_RX2_P	
B26	GND	A26	PCIEO_RX2_N	
B27	PCIEO_TX3_P	A27	GND	
B28	PCIEO_TX3_N	A28	GND	
B29	GND	A29	PCIEO_RX3_P	
B30	GND	A30	PCIEO_RX3_N	
B31	UARTO_TXD	A31	GND	
B32	UARTO_RXD	A32	GND	
B33	GND	A33	RFU	
B34	GND	A34	RFU	
B35	PCIE1_REFCLK_P	A25	GND	
B36	PCIE1_REFCLK_N	A36	GND	
B37	GND	A37	RFU	
B38	GND	A38	RFU	
B39	PCIE1_RESET#	A39	GND	
B40	RFU	A40	GND	
B41	GND	A41	RFU	
B42	GND	A42	NIC_SMBUS_ALERT#	
B43	NIC_SMBUS_SCL	A43	GND	
B44	NIC_SMBUS_SDA	A44	GND	
B45	GND	A45	GEO_RX_P	
B46	GND	A46	GEO_RX_N	
B47	GEO_TX_P	A47	GND	
B48	GEO_TX_N	A48	GND	
B49	GND	A49	RFU	
B50	GND	A50	RFU	
B51	RFU	A51	GND	





B52	RFU	A52	GND
B53	GND	A53	RFU
B54	GND	A54	RFU
B55	RFU	A55	GND
B56	RFU	A56	GND
B57	GND	A57	RFU
B58	GND	A58	RFU
B59	RFU	A59	GND
B60	RFU	A60	GND
B61	GND	A61	RFU
B62	GND	A62	RFU
B63	RFU	A63	GND
B64	RFU	A64	GND
B65	GND	A65	PCIE1_RX0_P
B66	GND	A66	PCIE1_RX0_N
B67	PCIE1_TX0_P	A67	GND
B68	PCIE1_TX0_N	A68	GND
B69	GND	A69	PCIE1_RX1_P
B70	GND	A70	PCIE1_RX1_N
B71	PCIE1_TX1_P	A71	GND
B72	PCIE1_TX1_N	A72	GND
B73	GND	A73	PCIE1_RX2_P
B74	GND	A74	PCIE1_RX2_N
B75	PCIE1_TX2_P	A75	GND
B76	PCIE1_TX2_N	A76	GND
B77	GND	A77	PCIE1_RX3_P
B78	GND	A78	PCIE1_RX3_N
B79	PCIE1_TX3_P	A79	GND
B80	PCIE1_TX3_N	A80	GND
B81	GND	A81	P12V
B82	GND	A82	P12V

Table 12: Intel CPU module connector pin out

#### **5.2.2 PCIe Interface**

PCI-Express Interface is used for management of the main board devices. They are connected as below.

CPU PCle Lane 0-1: BCM88375

CPU PCle Lane: I210



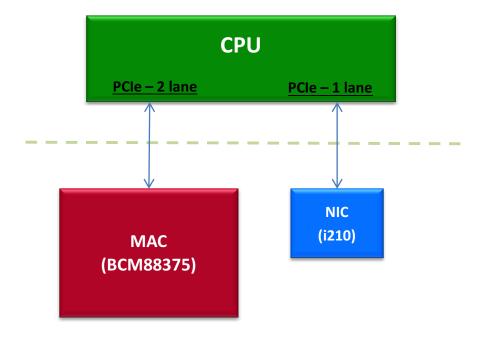


Figure 10: PCIe Connection

## **5.2.3 Ethernet Port Connectivity for Broadwell DE CPU**

The Ethernet ports on the CPU are connected as follows:

CPU PCIe : MGMT Port BMC MAC1 : MGMT Port

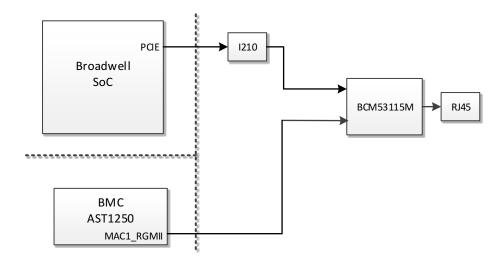


Figure 11: Ethernet Port Connection for Broadwell DE CPU



### 5.2.4 Ethernet Port Connectivity for Rangeley CPU

The Ethernet ports on the CPU are connected as follows:

CPU SGMII 0:

CPU SGMII 1: MGMT Port

CPU SGMII 2-3:

CPU PCIe : MGMT Port BMC MAC1 : MGMT Port

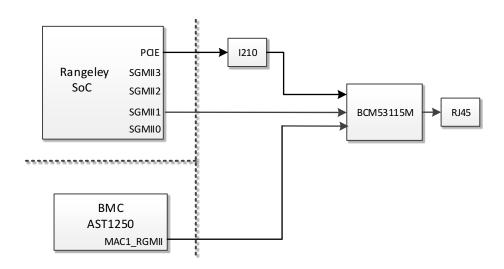


Figure 12: Ethernet Port Connection for Rangeley CPU

### **5.2.5 UART Port Connectivity**

The Rangeley has two on-chip UART interfaces supporting RS232 serial ports. UART0 is unused and UART1 is used to drive the console port.

The first UART controllers (UART1) can be assigned to function as Host System COM port.

CPU UART0 : Reserved

CPU UART1 : Serial console port (mini USB Type B)



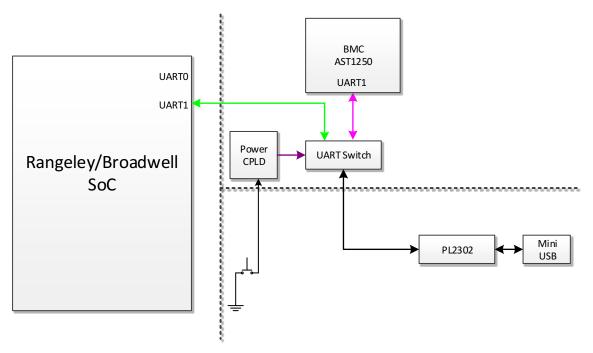


Figure 13: UART port connection

## **6 IO and Connectors**

#### 6.1 USB Interface

The CPU contains one Enhanced Host Controller Interface (EHCI) and complies to the EHCI 1.0 Specification. The EHCI supports up to four USB 2.0 root ports. USB 2.0 allows data transfers up to 480 Mbps. The controller integrates a Rate-Matching Hub (RMH) to support USB 1.1 devices. The USB Port 1 interface is configured by the debug software to be a debug port.

## 7 Power/Environmental/Agency Certifications

Power		
Number of power supply	2	
Power supply types	AC (forward and reversed airflow) DC (forward and reversed airflow)	
Typical operating power	377W	
Maximum power	645W	
AC PSUs <ul><li>Input voltage</li><li>Frequency</li><li>Efficiency</li></ul>	770W • 100 to 240 VAC • 50 to 60 Hz	
DC PSUs	1100W	



<ul><li>Input voltage range</li><li>Efficiency</li></ul>	• 40.5V/23.8A 48V/19.0A -60V/15.6			
Environment				
Operating temperature	0 ~ 45°C (at sea level with Fan Failure condition)			
Altitude	0 ~ 10,000ft at 40°C*			
Storage temperature	-40~70°C			
Operating relative humidity	0%-95% RH (non-condensing)			
Storage relative humidity	0%~95% RH (non-condensing)			
Dimensions (height x width x depth)	44 mm (H) x 441 mm (W) x 487.4 mm(D)			
Weight	TBD			

Table 13: Power consumption and environment table

Regulatory Standards Compliance				
Regulatory compliance Safety		Comply with CE markings per direct 2006/95/EC FCC/IC Report Class A BSMI UL/cUL Listed Mark CCC	tives 2004/1	L08/EC and
Safety		IEC60950-1 FCC/IC Report Class A EN 60950-1 FCC/IC Report Class A UL/CSA 60950-1 CNS 14336-1 GB4943.1		
EMC: Emissions		IEC60950-1 FCC/IC Report Class A EN 60950-1 FCC/IC Report Class A UL/CSA 60950-1 CNS 14336-1 GB4943.1		
		RoHS Requirement		
Level	Level # Description			Limitation / ppm
	A1	Cadmium/ Cadmium Compounds		80
	A2	Hexavalent Chromium/ Hexavalent Chromium Compounds		800
Α	А3	Lead/ Lead Compounds		800
	A4	Mercury/ Mercury Compounds		800
	A5	Polybrominated Biphenyls (PBBs)		800
A6		Polybrominated Diphenylethers (PBDEs)		800
		Reliability Test Reports		
Test Iten	ns	Standards	Remarks	



MTBF Prediction		
Report	Telcordia SR-332, Issue 2	
Free Fall Test Report	IEC 60068-2-32: Basic Environmental Testing Procedures Part 2: Tests Test Ed: Free Fall-Second Edition; Incorporating Amendment 1; Amendment 2: 10-1990	<ul> <li>Drop Range: ≤         20Kg,1000mm; ≤         50kg, 500mm;</li> <li>Max. Load: 500 kg;</li> <li>FCS: 1 carton.</li> </ul>
Vibration Test Report	IEC 60068-2-34:1973: Environmental testing Test Fd: Random vibration wide band-General requirements	<ul> <li>Frequency: 20Hz         ~500Hz;</li> <li>Method: Random;</li> <li>Test Time: 30 min/Per Axis;</li> <li>FCS: 1 carton.</li> </ul>
Storage Test Report	IEC 60068-2-48: Basic Environmental Testing Procedures Part 2: Tests Guidance on the Application of the Tests of IEC Publication 68 to Simulate the Effects of Storage-First Edition	<ul> <li>Low Temp.: -40     <sup>°</sup>C,72Hours</li> <li>High Temp. /Low     Humidity: 70     <sup>°</sup>C,25%R.H.,72Hours</li> <li>High Temp. /High     Humidity: 40     <sup>°</sup>C,95%R.H.,96Hours</li> <li>FCS: 1 carton</li> </ul>
Cold Test Report	IEC 60068-2-1: Environmental Testing Part 2: Tests - Tests A: Cold-Fifth Edition; Amendment 1-1993; Amendment 2-1994	<ul> <li>Temperature: -10±3         <sup>℃</sup></li> <li>Humidity:         Uncontrolled</li> <li>Test Time: 72 Hours</li> <li>FCS: 2 sets</li> </ul>
Dry Heat Cyclic Test Report	IEC 60068-2-2: Basic Environmental Testing Procedures Part 2: Tests - Tests B: Dry Heat-Fourth Edition; Supplement A-1976; Amendment 1-1993; Amendment 2-1994	<ul> <li>Temperature: 55±2°C</li> <li>Humidity: 5%R.H.</li> <li>Test Time: 72 Hours</li> <li>FCS: 2 sets</li> </ul>
Damp Heat Steady State Report	IEC 60068-2-78: Environmental Testing - Part 2-78: Tests - Test 2-78: Body Cab: Damp Heat, Steady State-First Edition; (Replaces IEC 60068-2-3 and 60068-2-56)	<ul> <li>Temperature: 40±2°C</li> <li>Humidity: 95+2-3%R.H.</li> <li>Test time: 96 Hours</li> <li>FCS: 2 sets</li> </ul>
Damp Heat Cyclic Report	IEC 60068-2-30: Basic Environmental Testing Procedures Part 2: Tests - Test Db and Guidance: Damp Heat, Cyclic	<ul> <li>Temperature: 40±2°C</li> <li>Humidity: 95+2-3%R.H.</li> <li>Cycle Time: 24 Hours</li> </ul>



	(12 + 12-Hour Cycle)-Second Edition; Amendment 1-08/1985	<ul><li>Number of Cycle: 2 Cycles</li><li>FCS: 2 sets</li></ul>
ESD Simulation Test Report	IEC 61000-4-2: Electromagnetic Compatibility (EMC) - Part 4-2: Testing and Measurement Techniques - Electrostatic Discharge Immunity Test-Edition 1.2; Edition 1:1995 Consolidated with Amendments 1:1998 and 2:2000	<ul> <li>Air Discharge: ±8KV;</li> <li>Contact Discharge: ±4KV;</li> <li>FCS: 1 set.</li> </ul>
Electrical Isolation Test Report	For Class I equipment only.	<ul> <li>Primary (L-N) to Earth (Metal frame or Ground Pin):         Minimum 1,5 kV ac, at least 60 seconds;</li> <li>Lan Port (RJ-45) to Ground (Metal frame) Minimum 1,5 kV ac at least 60 seconds.</li> </ul>

Table 14: Regulatory Standards Compliance table